HW / SW Performance Disconnect

- **SW Execution (Fast)**
  - 100,000 instructions/sec
- **HW Simulation (Slow)**
  - 1 instruction/sec

**Embedded Code**

- **Fast Runtime**
- **Comprehensive Verification**
Seamless Delivers Both Coverage and Performance

**SW Execution**

- Fast run-time
  - Direct memory operations to the Coherent Memory Server

**HW Simulation**

- Comprehensive verification
  - Boot and run code from the hardware

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**VSI Alliance (www.vsi.org)**
Seamless Coherent Memory Server

- Optimized Read/Write
- Memory Image Server
- Coherent Memory Server
- Logic Simulator
  - CPU
  - Dual Ported RAM
  - DSP
  - Memory Controller
  - IP Block
  - Custom Logic
  - Memory Block
  - I/O
  - Detailed Read/Write
Optimization are fully controlled by the user.

- Optimization set active button

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